

## EEE598D: Analog Filter & Signal Processing Circuits

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Today: Basic SC Circuit Building Blocks

- SC S/H Circuits
- SC Delay Circuits
- SC Gain Stage Circuits
- SC Integrator Circuits

### Sample-and-Hold Circuits

• S/H circuits are used to avoid continuous signal feed through which causes frequency response distortion.





### Sample-and-Hold Circuits

- Practical S/H Circuits
- Acquisition Time amplifier settling Switch settling - Slew Rate – Aperture uncertainty Time Slew rat **Feed through** - Hold-mode Droop Rate V<sub>o</sub> aquisition - Offset **Aperture delay**  Clock Feed Through (CFT) ph R ph Vo sample hold sample Vi  $C_h$

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### Sample-and-Hold Circuits

• CFT minimization technique



(a) Dummy switch

(b) Balanced dummy switch

### S/H Amplifier Structures

- Buffered S/H
  - Better drive capability
  - Better Isolation
  - Sensitive to offset



## Improved S/H Circuit

- Offset minimization
- 1/f noise minimization
- PSSR improvement
- Vo unavailable at ph1!!





## Offset & Parasitic Free S/H Circuit

• Alternative Approach



$$v_o = (1 - \varepsilon^2)v_i + \varepsilon(1 - \varepsilon)v_{off}$$

## Fully Differential S/H Circuit

- Cancellation of
  - Charge injection
  - Power supply noise
  - Common Mode Signal







## Delay Stage with Weighted Addition







# CFT Reduced Technique





#### SC Integrator Structure









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# Lossy (Damped) SC Integrator Structure

• Alternative (Positive Damped)



V<sub>o</sub>



## Multiple Inputs SC Integrator Structure

• Ideal Case



$$\frac{V_o(z)}{V_i(z)} = \sum_{i=1}^k \frac{C_i}{C_f} \frac{z^{-1/2} V_{i1}(z) - V_{i2}(z)}{(1 - z^{-1})}$$

# Bilinear SC Integrator Structure



$$\frac{V_o(z)}{V_i(z)} = -\frac{C_1(1+z^{-1})}{C_f(1-z^{-1})}$$

### 2-Step SC Integrator Structure



# 2-Step SC Integrator Structure

$$\frac{V_o(z)}{V_i(z)} = \left(\frac{C_1}{C_f} z^{-1/2}\right) \frac{1 + \frac{C_2}{C_1} z^{-1/2} + \frac{C_3}{C_1} z^{-1}}{(1 - z^{-1})}$$

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## Full Differential SC Integrator

- Charge Injection
- Power Supply Rejection
- High Speed
- Common Mode Rejection

# Full Differential SC Integrator Structure(I)



$$\frac{V_{o1}(z)}{V_{i}(z)} = \frac{C_{1}}{C_{F}} \frac{z^{-1/2}}{1 - z^{-1}}$$
$$\frac{V_{o2}(z)}{V_{i}(z)} = \frac{C_{1}}{C_{F}} \frac{1}{1 - z^{-1}}$$

# Full Differential SC Integrator Structure (II)



## Full Differential SC Integrator Structure (III)



$$\frac{V_o(z)}{V_i(z)} = \frac{C_1}{C_F} \frac{1 + z^{-1}}{1 - z^{-1}}$$



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## Full Differential SC Integrator Structure (V)



# Full Differential SC Integrator Structure (VI)

