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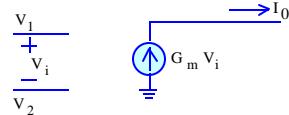
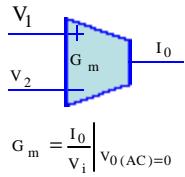
TRANSCONDUCTANCE AMPLIFIER TOPOLOGIES AND NON-IDEALITIES

- **Architectures and Macromodels***
 - **Linearized OTAs**
 - **OTA Non-Idealities**
 - Applications, i.e., OTA-C Filters, Oscillators, and VGA
- { Input impedance
Frequency dependent transconductance
Non-linear transconductance
Output impedance

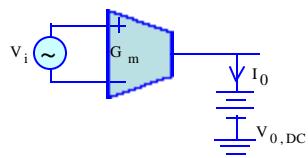
* Reference :

E. Sánchez-Sinencio and M.L. Majewski, "A Nonlinear Macromodel of Operational Amplifiers in the Frequency Domain", *IEEE Trans. Circuits and Systems*, Vol. CAS-26, No.6, pp 395-402, June 1979

OPERATIONAL TRANSCONDUCTANCE AMPLIFIER

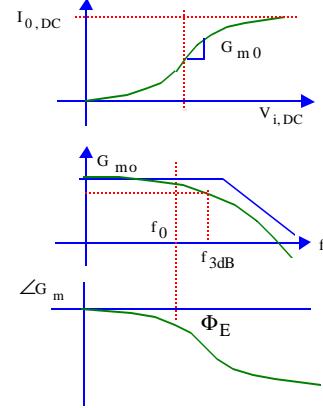


How do you simulate G_m ?

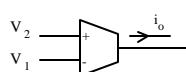


Φ_E is the phase difference between the actual G_m phase and the ideal phase.

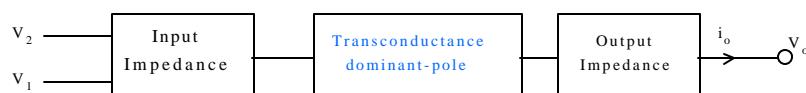
$$\omega_p = 2\pi f_{3\text{dB}}$$



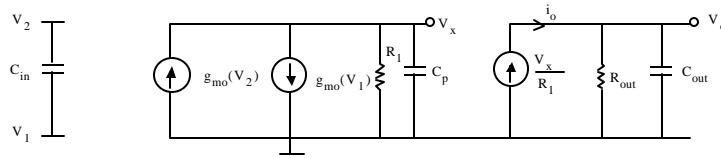
A Linear CMOS OTA Macromodel



$$g_m = \frac{g_{mo}}{1 + \frac{s}{w_p}} \quad ; \quad w_p = \frac{1}{R_i C_p}$$

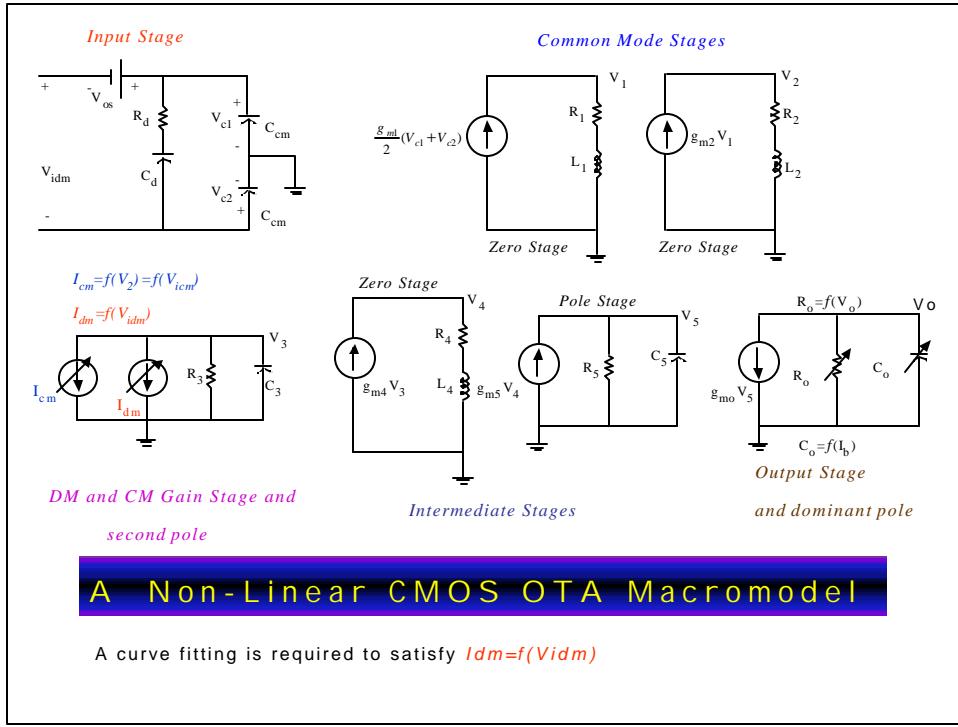


OTA Macromodel Representation



Reference:

A nonlinear macromodel for CMOS OTAs Gomez, G.J.; Embabi, S.H.K.; Sanchez-Sinencio, E.; Lefebvre, M. Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on Volume: 2 , 1995 Page(s): 920 -923 vol.2



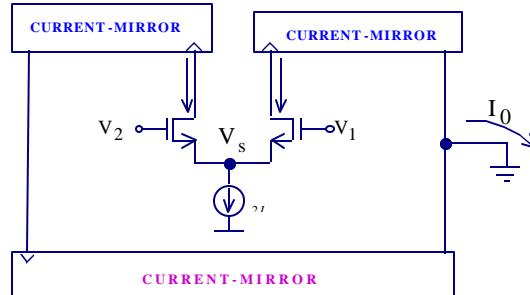
CALCULATED PARAMETER VALUES FOR NON-LINEAR MACROMODEL

Element	Value
C_{an}	27.88 fF
C_d	72.70 fF
R_d	2128.50 Ω
$R_1=R_2=R_3=R_4=R_5$	1 kΩ
$g_{ml}=g_{m2}=g_{m3}=g_{m4}=g_{mo}$	1 mmhos
L_1	125.60 μH
L_2	57.38 μH
C_3	2.83 pF
L_4	Not used
C_5	2.05 pF
R_o	2.17 MΩ
C_o	109.88 fF
I_{dm}	$g_{mdm}=590.04 \mu A/V$
I_{cm}	$g_{mcn}=56.41 nA/V$
V_{os}	-1.44 mV

SIMULATION DATA

Parameter	Simulated Value
G_{mdn}	DM Transconductance
G_{mem}	CM Transconductance
A_{vdm}	DM V/V Gain @ DC
$CMRK$	CM Rejection Ratio
R_o	Output Resistance
f_e	Phase Error @ 1MHz
W_{p1}	Dom. Pole (2 p_{p1}^f)
W_{p2}	2 nd pole (2 p_{p2}^f)
W_{p3}	3 rd pole (2 p_{p3}^f)
W_{z1}	Zero (2 p_{z1}^f)
W_{z2}	1 st CMZero (2 p_{z2}^f)
W_{z3}	2 nd CMZero (2 p_{z3}^f)
Z_{idm}	DM Z_n @ Real part
Z_{idm}	DM Z_n @ Imag.
Z_{im}	CM Z_n @ LF
Z_{im}	CM Z_n : Phase

Basic OTA (three current-mirrors): Non-linearity



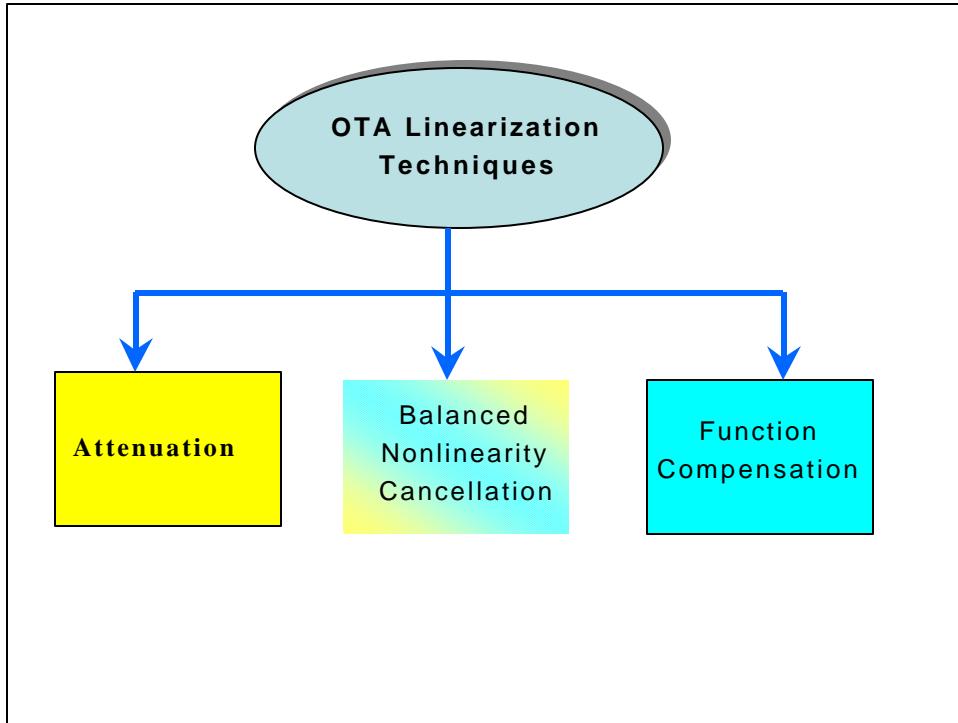
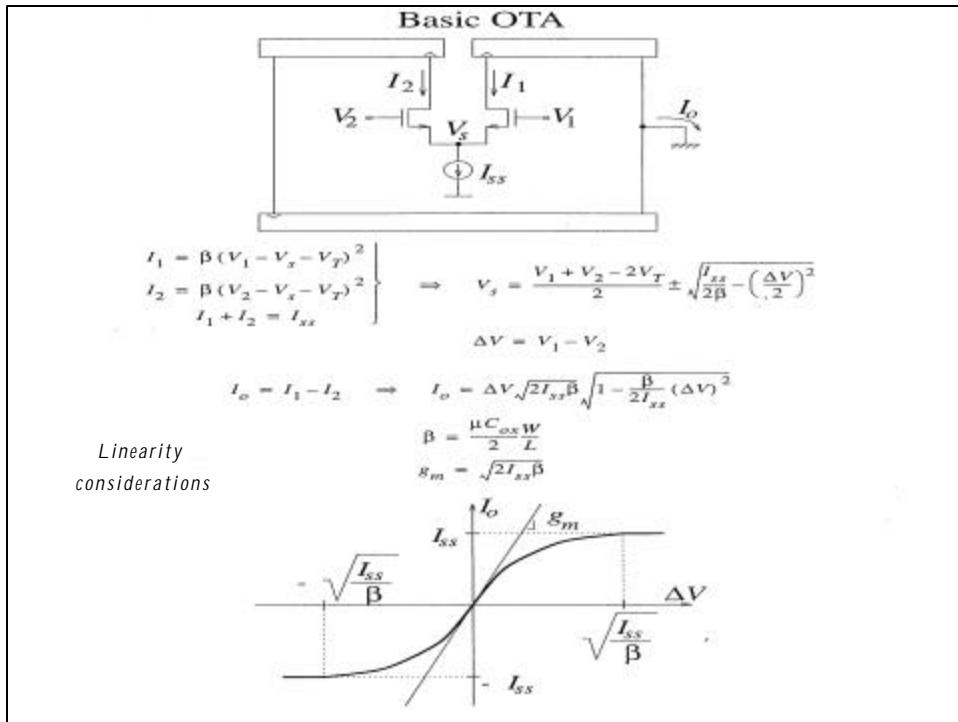
$$\left. \begin{aligned}
 I_1 &= \beta(V_1 - V_S - V_T)^2 \\
 I_2 &= \beta(V_2 - V_S - V_T)^2 \\
 I_1 + I_2 &= I_{ss}
 \end{aligned} \right\} \Rightarrow V_S = \frac{V_1 + V_2 - 2V_T}{2} \pm \sqrt{\frac{I_{ss}}{2\beta} - \left(\frac{\Delta V}{2}\right)^2}$$

$$\Delta V = V_1 - V_2$$

$$I_0 = I_1 - I_2 \Rightarrow I_0 = \Delta V \sqrt{\frac{I_{ss}}{2} \beta \left(1 - \frac{\beta}{\gamma_I} (\Delta V)^2\right)}$$

$$\beta = \frac{\mu C_{ox} W}{2 L}$$

$$g_m = \sqrt{2 I_{ss} \beta}$$



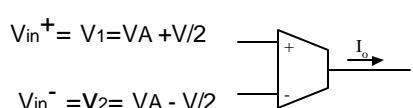
Remarks on Linearization Techniques

ATTENUATION

- Overall G_m is reduced by the attenuation factor a
- Non-idealities of the attenuation network affect the overall transconductance amplifier performance
- Relative input noise increase by a
- Input offset is proportional to a
- Dynamic range can be modified via tradeoffs.

Balanced Nonlinearity Cancellation

Type I :



$$\begin{aligned} I_0(V_1, V_2) &= \sum_{i=1}^{\infty} a_i (V_1^i - V_2^i) \\ &= a_1 (V_1 - V_2) + a_2 (V_1^2 - V_2^2) \\ &\quad + a_3 (V_1^3 - V_2^3) + \dots \end{aligned}$$

$$\begin{aligned} I_0(V) &= V(a_1 + 2a_2 V_A + 3a_3 V_A + \dots) \\ &\quad + \frac{1}{4} V^3 (a_3 + 4a_4 V_A + 10a_5 V_A + \dots) \\ &\quad + \frac{1}{16} V^5 (a_5 + 6a_6 V_A + \dots) \end{aligned}$$

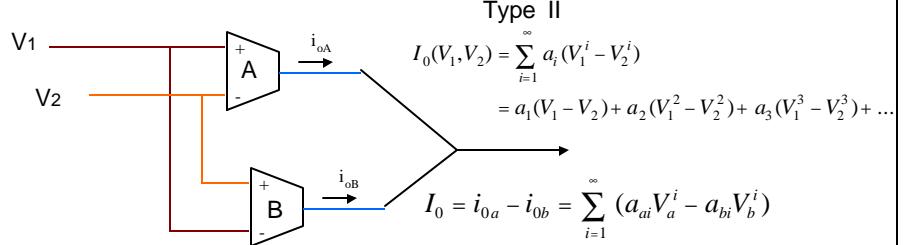
For small a_3, a_4, a_5, \dots

$$I_0 \approx g_m V$$

where

$$g_m = a_1 + 2a_2 V_A^1 + 3a_3 V_A^2 + \dots$$

Balanced Nonlinearity Cancellation



Particular cases:

For matched transconductors

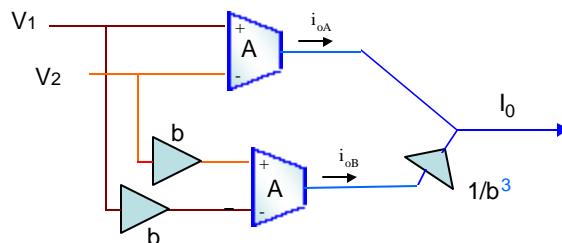
$$a_{ai} = a_{bi} = a_i$$

$$V \equiv V_1 - V_2$$

$$I_0 = 2(a_1V + a_3V^3 + a_5V^5 + \dots)$$

Assume each OTA has the following type II non-linearity description:

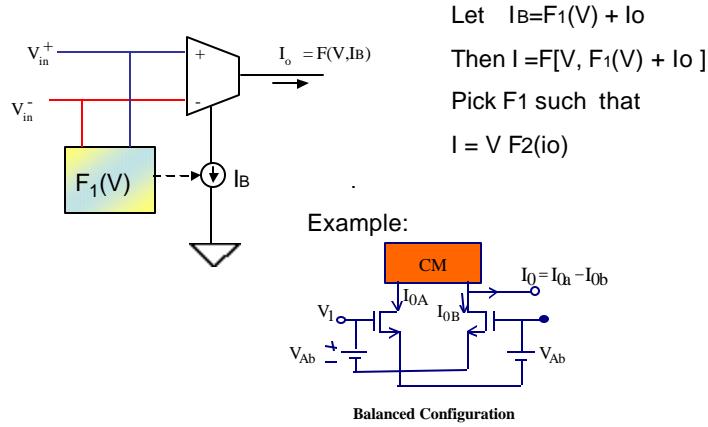
$$i_{oA} = A V_{in} (1 - a V_{in}^2) = a_1 V_{in} - a_2 V_{in}^3$$



Equal Transconductance gains can be used, but one of them modified at the input and output. Then the linearized output current yields:

$$I_o = A(1 - \frac{1}{b^2})V_{in}$$

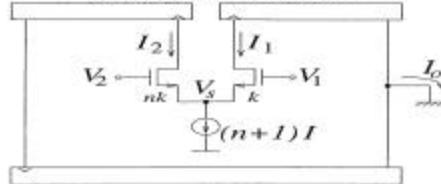
Function Compensation



Nedungadi Transconductor

[A. Nedungadi and T. R. Viswanathan, "Design of Linear CMOS Transconductor Elements," *IEEE Transactions on Circuits and Systems*, vol. CAS-31, No. 10, pp. 891-894, October 1984]

Unbalanced Differential Pair



$$\begin{aligned} I_1 &= k(V_1 - V_s - V_T)^2 \\ I_2 &= nk(V_2 - V_s - V_T)^2 \\ I_1 + I_2 &= (n+1)I \end{aligned} \quad \left\{ \Rightarrow V_s^2 - 2V_s \left(\frac{V_1 + nV_2}{n+1} - V_T \right) + \frac{(V_1 - V_T)^2 + n(V_2 - V_T)^2}{n+1} = \frac{I}{k} \right.$$

$$V_s = \frac{V_1 + nV_2 - (n+1)V_T}{n+1} - \sqrt{\frac{I}{k} - \frac{n\Delta V^2}{(n+1)^2}} \Rightarrow$$

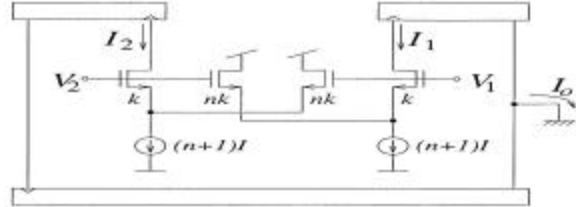
$$\Rightarrow \begin{cases} I_1 = k \left[\frac{n\Delta V}{n+1} + \sqrt{\frac{I}{k}} \sqrt{1 - \frac{k(n\Delta V)^2}{(n+1)^2}} \right]^2 \\ I_2 = nk \left[\frac{-\Delta V}{n+1} + \sqrt{\frac{I}{k}} \sqrt{1 - \frac{k(n\Delta V)^2}{(n+1)^2}} \right]^2 \end{cases}$$

Range:

$$\begin{cases} I_1 = (n+1)I \\ I_2 = 0 \end{cases} \Rightarrow \Delta V = \sqrt{\frac{I}{k}(n+1)}$$

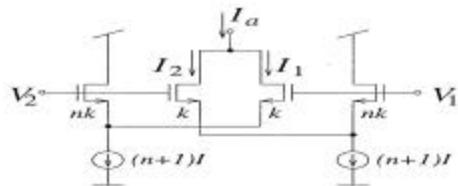
$$\begin{cases} I_1 = 0 \\ I_2 = (n+1)I \end{cases} \Rightarrow \Delta V = -\sqrt{\frac{I}{k}(1+\frac{1}{n})}$$

The Cross-Coupled Quad Transconductor

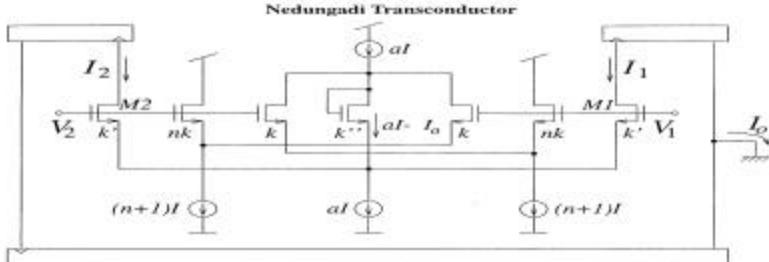


$$\begin{aligned}
 I_1 &= k \left[\frac{n\Delta V}{n+1} + \sqrt{\frac{I}{k}} \sqrt{1 - \frac{k n \Delta V^2}{(n+1)^2 I}} \right]^2 \\
 I_2 &= k \left[\frac{-n\Delta V}{n+1} + \sqrt{\frac{I}{k}} \sqrt{1 - \frac{k n \Delta V^2}{(n+1)^2 I}} \right]^2 \\
 \Rightarrow I_o = I_1 - I_2 &\Rightarrow \begin{cases} I_o = g_m \Delta V \sqrt{1 - \beta \Delta V^2} \\ g_m = \frac{4n}{n+1} \sqrt{Ik} \quad , \quad \beta = \frac{k}{I} \frac{n}{(n+1)^2} \end{cases} \\
 \text{Range: } |\Delta V| &\leq \sqrt{\frac{I}{k} \left(1 + \frac{1}{n} \right)} \\
 \text{For } \Delta V = \sqrt{\frac{I}{k} \left(1 + \frac{1}{n} \right)} &\Rightarrow I_o = \frac{4n}{n+1} I
 \end{aligned}$$

Square



$$\begin{aligned}
 I_1 &= k \left[\frac{n\Delta V}{n+1} + \sqrt{\frac{I}{k}} \sqrt{1 - \frac{k n \Delta V^2}{(n+1)^2 I}} \right]^2 \\
 I_2 &= k \left[\frac{-n\Delta V}{n+1} + \sqrt{\frac{I}{k}} \sqrt{1 - \frac{k n \Delta V^2}{(n+1)^2 I}} \right]^2 \\
 \Rightarrow I_a = I_1 + I_2 &= 2k \frac{n(n-1)}{(n+1)^2} \Delta V^2 + 2I \quad , \quad \Delta V \leq \sqrt{\frac{I}{k} \left(1 + \frac{1}{n} \right)}
 \end{aligned}$$



Transistors M1 and M2 form a simple differential pair biased by

$$I_{ss} = af - (af - I_a) = I_a = \frac{2k^2(n-1)}{(n+1)^2} \Delta V^2 + 2I$$

Consequently,

$$I_o = \Delta V \sqrt{2k' I_{ss} - k'^2 \Delta V^2} = \Delta V \sqrt{k'^2 \Delta V^2 \left(\frac{4\gamma^2(n-1)}{(n+1)^2} \right) + 4k'I} , \quad \gamma = \frac{k}{k'}$$

$$\text{If } 4\gamma n(n-1) - (n+1)^2 = 0 \Rightarrow I_o = 2\Delta V \sqrt{k'I} \quad \begin{cases} g_m = 2\sqrt{k'I} \\ |\Delta V| \leq \min\left(\sqrt{\frac{I}{k}}\left(1 + \frac{1}{n}\right), 2\sqrt{\frac{I}{k'}}\right) \end{cases}$$

Examples:

$$\gamma = 1 \Rightarrow n = 1 + \frac{2\sqrt{3}}{3} = 2.155$$

$$n = 3 \Rightarrow \gamma = \frac{(n+1)^2}{4n(n-1)} = \frac{2}{3}$$

$$\alpha I - I_a \geq 0 \Leftrightarrow \begin{cases} \Delta V_{max}^2 = \frac{I_a}{k'} \rightarrow \alpha \geq \frac{4n}{n+1} \\ \Delta V_{max}^2 = \frac{I}{k} \rightarrow \alpha \geq 2 \left[\frac{(1+4\gamma)n^2 + 2n(1-2\gamma) + 1}{(n+1)^2} \right] \end{cases}$$

Distortion Analysis

$i_d = i_d(v_{in})$ Suppose that i_d is a non-linear function, then

$$i_d = I_D + \frac{\partial i_d}{\partial v_{in}} \Big|_Q v_{in} + \frac{1}{2} \frac{\partial^2 i_d}{\partial v_{in}^2} \Big|_Q v_{in}^2 + \frac{1}{6} \frac{\partial^3 i_d}{\partial v_{in}^3} \Big|_Q v_{in}^3 + \dots$$

for $v_{in} = V \sin(\omega t)$

$$i_d \equiv I_D + \frac{1}{4} \frac{\partial^2 i_d}{\partial v_{in}^2} \Big|_Q V^2 + \frac{\partial i_d}{\partial v_{in}} \Big|_Q + \frac{\partial^3 i_d}{\partial v_{in}^3} \Big|_Q \frac{V^2}{8} + \dots V \sin(\omega t) +$$

$$+ \frac{1}{4} \frac{\partial^2 i_d}{\partial v_{in}^2} \Big|_Q V^2 \sin(2\omega t) + \frac{1}{24} \frac{\partial^3 i_d}{\partial v_{in}^3} \Big|_Q V^3 \sin(3\omega t) + \dots$$

CONTINUOUS-TIME FILTERS

Transconductance

$$g_m = \frac{\partial i_d}{\partial v_{in}}_Q + \frac{\partial^3 i_d}{\partial v_{in}^3}_Q \frac{V^2}{8} + \dots$$

Second Harmonic Distortion

$$HD_2 \approx \frac{1}{4} \frac{\frac{\partial^2 i_d}{\partial v_{in}^2}_Q V^2}{\frac{\partial i_d}{\partial v_{in}}_Q}$$

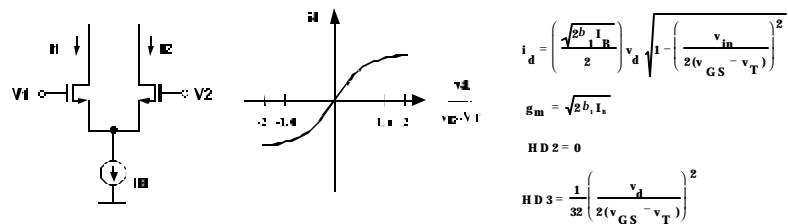
Third Harmonic Distortion

$$HD_3 \approx \frac{1}{24} \frac{\frac{\partial^3 i_d}{\partial v_{in}^3}_Q V^3}{\frac{\partial i_d}{\partial v_{in}}_Q}$$

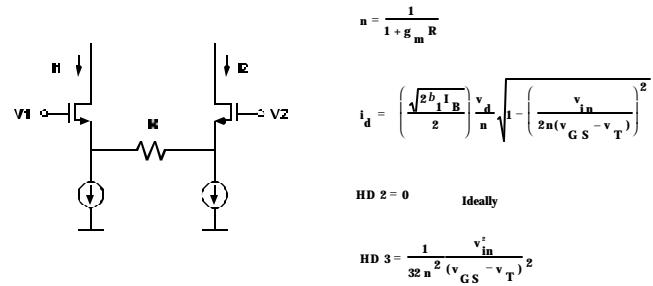
Third Intermodulation Distortion

$$IM_3 = 3 * HD_3$$

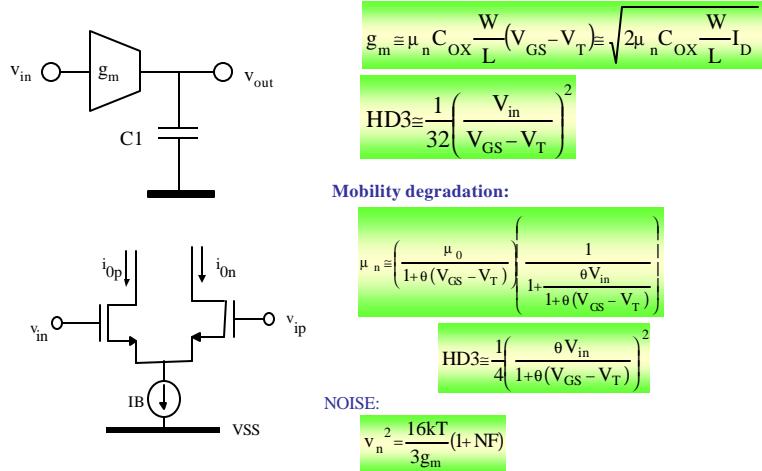
Differential Pair as a V-I converter



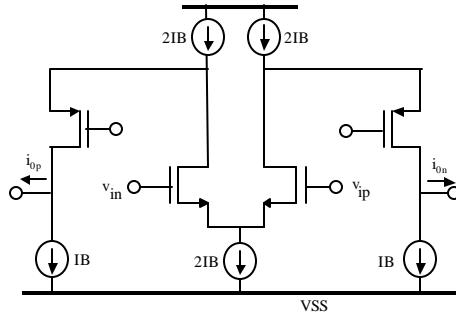
Differential Pair with Source Degeneration



BASIC INTEGRATOR: Fundamental Equations



FOLDED-CASCODE OTA: Design Equations



$$\text{POWER: } P = 4I_B(V_{DD} + V_{SS})$$

$$\text{NOISE: } v_n^2 = \frac{16kT}{3g_m}(1 + NF)$$

TRANSCONDUCTANCE:

$$g_m = \frac{g_{mo}}{1 + \frac{C_p}{g_{mp}}}$$

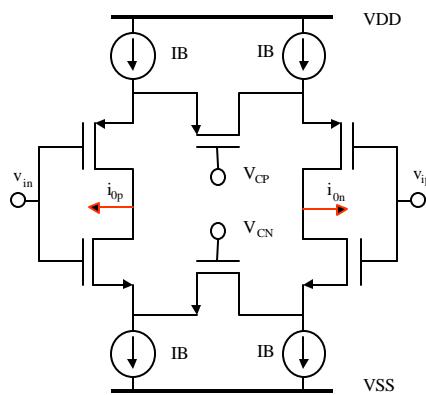
$$\text{INPUT SWING: } V_{SS} + 2V_{DSAT} + V_T > V_{IN}$$

OUTPUT RESISTANCE:

$$\text{OUTPUT SWING: } V_{SS} + 2V_{DSAT} < V_{OUT} < V_{DD} - 2V_{DSATP}$$

$$R_o \approx g_{mn} R_{ON}^2 \left| g_{mp} R_{OP} \right|^2$$

Single stage OTA based on complementary differential pairs



$$P = I_B(V_{DD} + V_{SS})$$

$$G_m = \frac{g_{mn}}{1 + N_N} + \frac{g_{mp}}{1 + N_P}$$

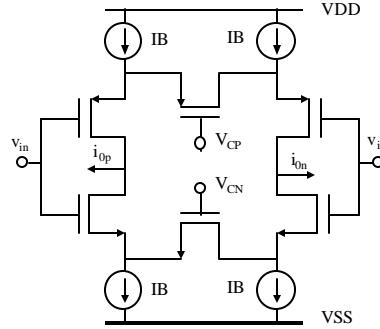
$$v_n^2 = \frac{16kT}{3G_m}(1 + NF)$$

OUTPUT SWING IS LIMITED:

$$V_{IN} - V_{TN} < V_{OUT} < V_{IN} + V_{TP}$$

Need to make sure no drivers operate in the ohmic region.

OTA based on Complementary Differential Pairs



TRANSCONDUCTANCE:

$$G_m = \frac{g_{mn}}{1 + N_N} + \frac{g_{mp}}{1 + N_P}$$

DISTORTION:

$$HD3 \equiv \frac{1}{32} \left(\frac{V_{in}}{(V_{GS} - V_T)(N+1)} \right)^2$$

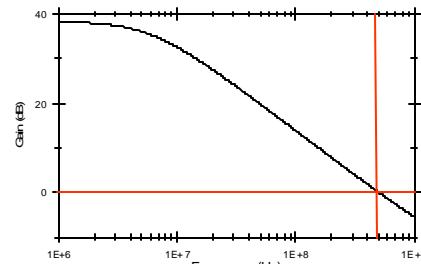
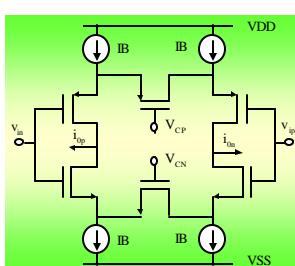
OUTPUT RESISTANCE:

$$R_{out} \approx \frac{g_{mn} r_{on} R_N}{2} \parallel \frac{g_{mp} r_{op} R_P}{2}$$

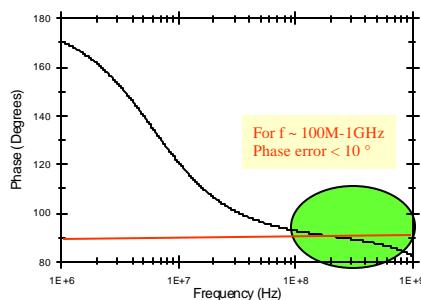
DC GAIN:

$$A_V \approx A_{VN}$$

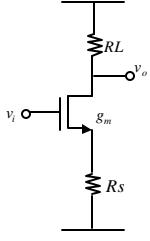
OTA Results: $0.35\text{ }\mu\text{m}$ Process



Transistor	W, L	Bias current
MN	$200, 0.6\text{ }\mu\text{m}$	$200\text{ }\mu\text{A}$
MCN	$15, 0.6\text{ }\mu\text{m}$	$0\text{ }\mu\text{A}$
MN	$200, 0.6\text{ }\mu\text{m}$	$200\text{ }\mu\text{A}$
MCN	$30, 0.6\text{ }\mu\text{m}$	$0\text{ }\mu\text{A}$



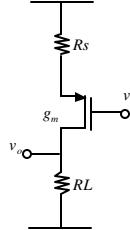
Wide Output and Good Linearity Observations



N-driver Transconductor

$$G_m = \frac{g_m}{1 + g_m R_s} = \frac{1}{\frac{1}{g_m} + R_s}$$

$$A_v = -G_m R_L = \frac{-R_L}{\frac{1}{g_m} + R_s}$$

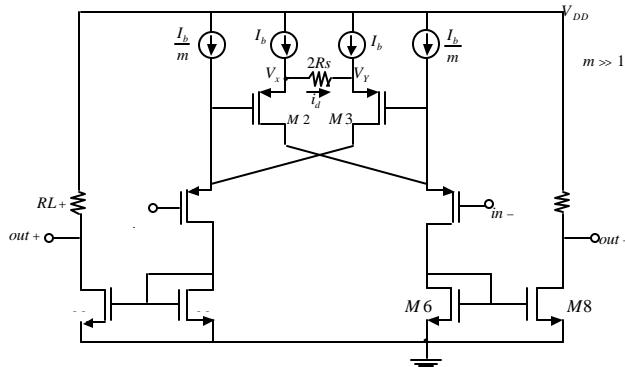


P-driver Transconductor

- In order to make A_v or G_m independent of g_m , it is required that $R_s \gg 1/g_m$
- Large R_s take up large area and have large parasitic capacitances which limit the amplifier bandwidth.
- It would be desirable to have A_v (G_m) independent of g_m with small R_s

Next we introduce a wideband voltage amplifier, however a similar approach might be used to derive a transconductance amplifier whose G_m could be nearly $1/R$ without requiring a large g_m associated with driver transistor.

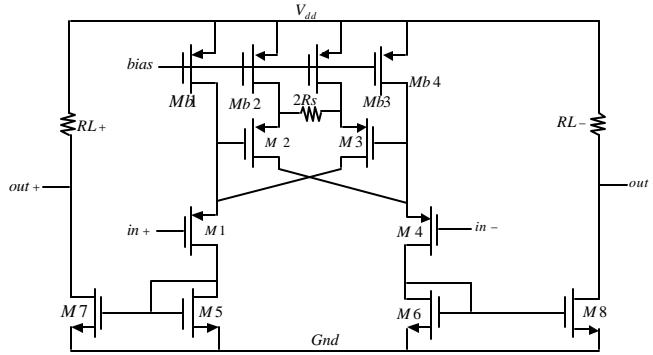
Wide Output Swing and Improved Linearity Amplifier



- This circuit is capable to cancel the effect of g_{m1}, g_{m4}
- We need to force the DC components of V_x and V_y to be equal, such that the current through $2R_s$ is only a function of the differential input $v_{in}^+ - v_{in}^- = v_d$, thus i_d becomes $v_d/2R_s$.
- Condition to satisfy

$$v_{gs_1} + v_{gs_2} = v_{gs_4} + v_{gs_3} \Rightarrow V_x = V_y$$

Amplifiers with Wide Output Swing and Good Single-Ended Linearity A Circuit Implementation.



Resistively loaded MOS amplifier with g_m cancellation.

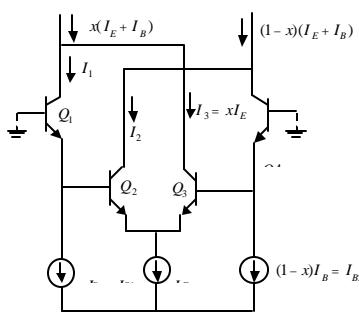
$$V_{gs_1} + V_{gs_2} = V_{gs_3} + V_{gs_4}$$

$$i_{2RS} = (v_{in}^+ - v_{in}^-)/2R_s = v_d/2R_s$$

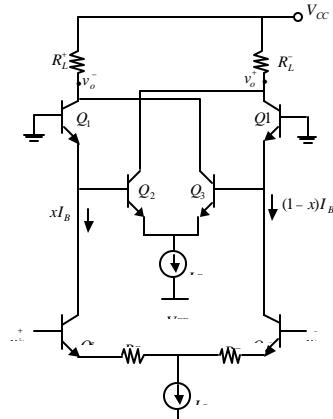
If $I_{b1,b4} \ll I_{b2,b3}$ then $A_s \equiv \frac{R_L}{R_s} \frac{(W/L)_7}{(W/L)_5}$

A Bipolar Amplifier version is next shown.

This Circuit is called Gilbert Gain Cell.



$$\begin{aligned} \frac{I_2}{I_3} &= \frac{I_{B2}}{I_{B1}} = \frac{I_4}{I_1} \\ I_{o1} &= (I_1 + I_3) - (I_2 + I_4) \\ A_I &= \frac{I_o}{I_{in}} = \frac{I_B + I_E}{I_B} \end{aligned}$$



$$A_V = \frac{v_o}{v_{in}} = \frac{R_L}{R_E} \left(1 + \frac{I_E}{I_B}\right)$$

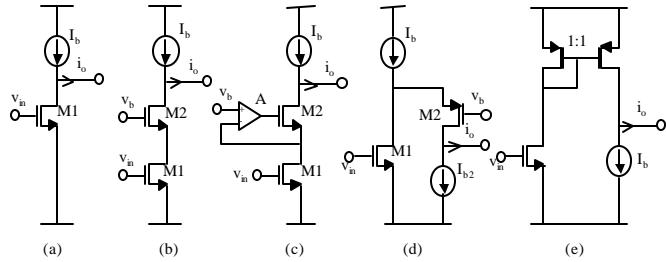
The dominant pole is placed at

Reference

$$W^p = \overline{WT}$$

A.B. Grebene, "Bipolar and MOS Analog Integrated Circuit Design", John Wiley & Sons, New York 1984.

Single-input Transconductor Implementations



Single Input (a) Negative Simple Transconductor, (b) Cascode Transconductor, (c) Enhanced Transconductor, (d) Folded-Cascode Transconductor, (e) Positive Simple Transconductor.

- Observe that:
 $g_m = f(I_b)$, the exact relation is a function of the transistor region of operation.
- Note that output impedance of (a) is only $1/g_{ds}$ and (b) and (c) implementations have larger output impedances.

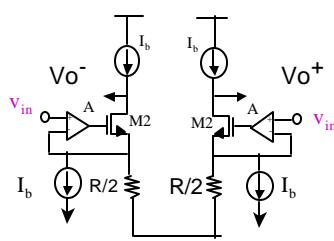
Properties of Simple Transconductors

Structure/ Figure	R_{out}	Min V_{DD}^*
Simple/l(a)	$\frac{1}{g_{ds1}}$	$\sqrt{\frac{2I_B}{k}} + V_{sat,I_B}$
Cascode/l(b)	$\frac{g_{m2}}{g_{dd} g_{ds2}}$	$(1+m)\sqrt{\frac{2I_B}{k}} + V_{sat,I_B}$
Enhanced/l(c)	$\frac{A g_{m2}}{g_{ds1} g_{ds2}}$	$(1+m)\sqrt{\frac{2I_B}{k}} + V_{sat,I_B}$
Folded/l(d)	$\frac{g_{m2}}{g_{ds1} g_{ds2}}$	$\sqrt{\frac{2I_B}{k}} + V_{Tp} + V_{sat,I_B}$

* The bottom devices of the cascode pairs have an aspect ratio of $(W/L)_1/(W/L)_2 = m^2$. k is a technological parameter determined by the mobility, and the gate oxide; V_{sat,I_B} is the saturation voltage for the I_B current source.

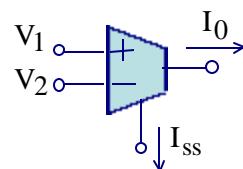
A Transconductance Amplifier with $G_m = 1/R$

An option to have a transconductance proportional to $1/R$ without requiring a large g_{m2}



Active Frequency Compensation Transconductor

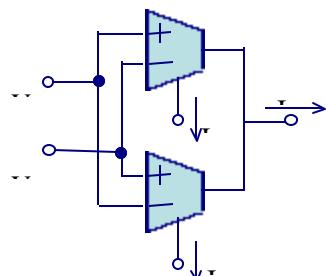
[J. Ramirez-Angulo and E. Sanchez-Sinencio, "Active Compensation of Operational Transconductance Amplifier Filters Using Partial Positive Feedback," IEEE Journal of Solid-State Circuits, vol. 25, No. 4, pp. 1024-1028, August 1990]



$$I_0 = g_m(V_1 - V_2)$$

$$g_m(s) = g_{m0} \left(1 - \frac{s}{\omega} \right)$$

ω depends on I_{ss}



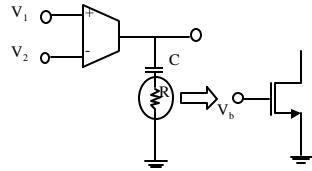
$$I_0 = (g_{mp}(s) - g_{mN}(s)) \Delta V = g_{meff}(s) \Delta V$$

$$g_{meff}(s) = g_{meff0} \left[1 - \frac{s}{\omega_{eff}} \right]$$

$$g_{meff0} = g_{mP0} - g_{mN0}, \quad \omega_{eff} = \frac{g_{meff0}}{\frac{\omega_p}{g_{mP0}} - \frac{\omega_N}{g_{mN0}}}$$

It is possible to make

Passive OTA Excess Phase Compensation

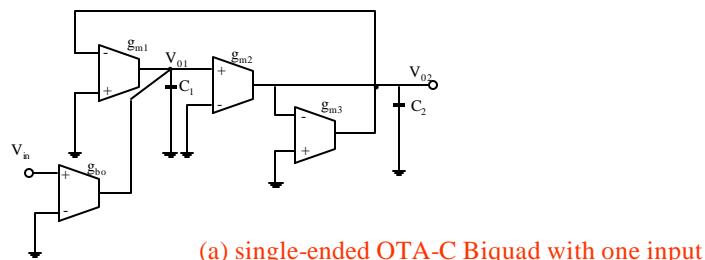


How to determine the value of RC ?

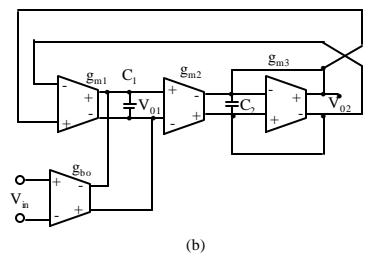
The R is implemented with a transistor operating in the triode (ohmic) region.

The zero generated by the RC should cancel the dominant pole of $G_m(s)$.

Two-integrator biquads



(a) single-ended OTA-C Biquad with one input

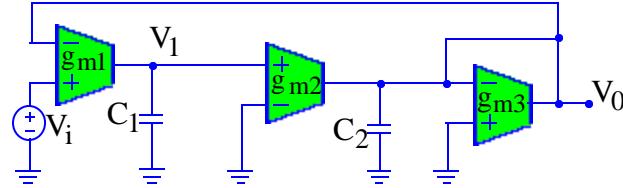


(b)

Fully differential OTA-C Biquad

Analog and Mixed Signal Center, TAMU

OTA Specifications in Open Loop Applications



$$V_1 = \frac{1}{sC_1} g_{m1} (V_i - V_0) \quad (1)$$

$$V_0 = (g_{m2} V_1 - g_{m3} V_0) \frac{1}{sC_2} \quad (2)$$

(1) into (2)

$$(sC_2)V_0 = \left[g_{m2} \frac{g_{m1}}{sC_1} (V_i - V_0) - g_{m3} V_0 \right]$$

$$V_0 \left[sC_2 + \frac{g_{m1}g_{m2}}{sC_1} + g_{m3} \right] = \frac{g_{m1}g_{m2}}{sC_1} V_i$$

$$H_{LP}(s) = \frac{V_0}{V_i} = \frac{\frac{g_{m1}g_{m2}}{sC_1C_2}}{s^2C_1C_2 + sC_1g_{m3} + g_{m1}g_{m2}} = \frac{\frac{g_{m1}g_{m2}}{C_1C_2}}{s^2 + s\frac{g_{m3}}{C_2} + \frac{g_{m1}g_{m2}}{C_1C_2}}$$

$$\omega_o^2 = \frac{g_{m1}g_{m2}}{C_1C_2} \quad , \quad BW = \frac{\omega_o}{Q} = \frac{g_{m3}}{C_2}$$

$$Q = \frac{1}{g_{m3}} \sqrt{\frac{g_{m1}g_{m2}C_2}{C_1}} = \frac{C_2\omega_o}{g_{m3}}$$

Now let's assume the transconductance is characterized by:

$$g_m = g_{m0} e^{-s/\omega_p} \approx g_{m0} (1 - s/\omega_p) \text{ for } \omega_p \ll \omega_0.$$

Under this condition the excess phase can be expressed as $\phi_E \approx \omega_0/\omega_p$.

Note that ideally $\phi_E = 0^0$.

then,

$$\begin{aligned} H_{LP}(s) &= \frac{g_{m0} g_{m0} 2 (1 - s/\omega_{p1})(1 - s/\omega_{p2})}{s^2 C_1 C_2 + s C_1 g_{m0} 3 (1 - s/\omega_{p3}) + g_{m0} 1 g_{m0} 2 (1 - s/\omega_{p1})(1 - s/\omega_{p2})} \\ D(s) &= s^2 C_1 C_2 + s C_1 g_{m0} 3 - s^2 \frac{C_1 g_{m0} 3}{\omega_{p3}} + g_{m0} 1 g_{m0} 2 \left\{ 1 - s \left(\frac{1}{\omega_{p1}} + \frac{1}{\omega_{p2}} \right) + \frac{s^2}{\omega_{p1} \omega_{p2}} \right\} \\ D(s) &= s^2 \left\{ C_1 C_2 - \frac{C_1 g_{m0} 3}{\omega_{p3}} + \frac{g_{m0} 1 g_{m0} 2}{\omega_{p1} \omega_{p2}} \right\} + s \left\{ C_1 g_{m0} 3 - \left(\frac{1}{\omega_{p1}} + \frac{1}{\omega_{p2}} \right) g_{m0} 1 g_{m0} 2 \right\} + g_{m0} 1 g_{m0} 2 \end{aligned}$$

Then the actual ω_{oa} and BW_a become

$$\begin{aligned} \omega_{oa}^2 &= \frac{g_{m0} 1 g_{m0} 2}{C_1 C_2 + \frac{g_{m0} 1 g_{m0} 2}{\omega_{p1} \omega_{p2}} - \frac{C_1 g_{m0} 3}{\omega_{p3}}} \\ BW_a &= \frac{\omega_{oa}}{Q_a} = \frac{C_1 g_{m0} 3 - \left(\frac{1}{\omega_{p1}} + \frac{1}{\omega_{p2}} \right) g_{m0} 1 g_{m0} 2}{C_1 C_2 - \frac{C_1 g_{m0} 3}{\omega_{p3}} + \frac{1}{\omega_{p1} \omega_{p2}}} \end{aligned}$$

Let us also assume that $\omega_{p1} = \omega_{p2} = \omega_p$, then $\omega_{oa} \approx \omega_0$, thus,

$$\begin{aligned} BW_a &= \frac{\omega_{oa}}{Q_a} = \frac{C_1 g_{m0} 3 - \frac{2}{\omega_p} g_{m0} 1 g_{m0} 2}{C_1 C_2 - \frac{C_1 g_{m0} 3}{\omega_p} + \frac{1}{\omega_p^2}} \approx \frac{C_1 g_{m0} 3 - \frac{2}{\omega_p} g_{m0} 1 g_{m0} 2}{C_1 C_2} \\ BW_a &\approx \frac{g_{m0} 3}{C_2} - \frac{g_{m0} 1 g_{m0} 2}{C_1 C_2} \frac{2}{\omega_p} = BW - \omega_{oa}^2 \cdot \frac{2}{\omega_p} = BW - \frac{2\omega_{oa}^2}{\omega_p} \\ Q_a &= \frac{\omega_{oa}}{BW_a} \approx \frac{1 \cdot \omega_{oa}}{\frac{g_{m0} 3}{C_2} - \frac{\omega_{oa}^2}{\omega_p}} \\ Q_a &= \frac{\frac{C_2 \omega_{oa}}{g_{m0} 3}}{1 - \frac{C_2 \omega_{oa} \cdot 2\omega_{oa}}{g_{m0} 3 \cdot \omega_p}} = \frac{Q}{1 - \frac{Q_2 \omega_{oa}}{\omega_p}} = \frac{Q}{1 - \frac{2\omega_{oa}}{\omega_p} Q} \end{aligned}$$

Alternatively, Q_a can be expressed in terms of the excess phase $\phi_E = \tan^{-1} \frac{\omega_0}{\omega_p} \approx \frac{\omega_0}{\omega_p}$ then

$$Q_a \approx \frac{Q}{1 - 2\phi_E Q} \approx Q(1 + 2\phi_E Q)$$

Furthermore, if $A_{vo} = g_m R_o$ is taken into account, then

$$Q_a = \frac{Q}{1 + \frac{2Q}{A_{vo}}}$$

If $A_{vo} = 500$

$$Q_a = \frac{Q}{1 + 4 \times 10^{-3} Q}$$

Note that :

$Q_a \downarrow$ when $A_{vo} \downarrow$	Q	Q_a
	1	0.996
	5	4.902
	10	9.6
	50	41.667

$$BW_a \downarrow \quad Q_a \uparrow \quad \text{when } \phi_E \uparrow$$